

CIL
CNU CRITICAL ITEMS LIST

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12/24/91 SUPERSEDES 06/31/90

ANALYST:

NAME P/N QTY	CRIT	FAILURE MODE & CAUSED	FAILURE EFFECT	RATIONALE FOR ACCEPTANCE
CAUTION AND WARNING SYSTEM, ITEM 150 SV785970-13 (1)	2/2	1500H081 Failure of ROM or core read/write memory locations.	END ITEM: Erroneous processing of CWS data.	<p>A. Design -</p> <p>Established reliability capactities and registers are qualified to the requirements of applicable military specification and thermal shocked per Condition B Test Method 107 of MIL-SID-202.</p> <p>Microcircuits are qualified to the requirements of MIL-M-30510 and receive the burn-in of Class B parts per method 3004 of MIL-SID-883.</p> <p>Transistors, diodes are qualified to the requirements of MIL-S-19500 and receive the burn-in of JANTRV Level parts per the applicable methods, 103B, 103B, 1040 or MIL-SID-730. The electronic components are operating within the power derating requirements of MILS 7004.</p> <p>The printed circuit (PC) board are fiberglass/epoxy per MIL-P-13060 type of and manufactured in accordance with MSPEC-SID-354. Parts mounting and soldering is per MSPEC-SID-356 and MILS300.4 (3A-1).</p> <p>The CWS is a mother/daughter board assembly. The daughter boards are held in place by metal card guides which also provide thermal transfer from the board heatlinks to the case. The top cover of the CWS exerts a downward force on the daughter boards to keep them properly seated in the mother board connectors.</p> <p>Flex tape (Kapton insulated, flexible flat conductor) instead of conventional Teflon coated wires is used to provide connections between the mother board and the external connectors. This prevents pinching of the conductor during item assembly. The PC board assemblies are conformal coated per MIL-A-46166 (Dow Corning RTV 3140) for environmental and humidity protection. Electrical connectors are environmentally sealed to prevent damage due to contamination and humidity.</p> <p>B. Test -</p> <p>Component Acceptance Test -</p> <p>FULL functioning of the CWS is verified during Item AIF Tests including continuity, logic flow, N-state sequencing, fault simulation, verification of status and fault messages, warning and alert tones activation, and BTIE activation. These tests are conducted upon completion of random vibration testing.</p> <p>PDA Test -</p> <p>The above electrical tests are repeated during PLSS PDA to</p>

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P/N	MODE &			
QTY	DRIV	CAUSES	FAILURE EFFECT	RATIONALE FOR ACCEPTANCE
2/2		ISOVRD8;		

verify CNS operation. The CNS is also operational during other PLSS PDA electrical tests such as sensor accuracy checks, Item 123 fan operation, Item 174 RIDS checkout, and solenoid valve actuation.

Certification -

The item completed the 15 year structural vibration and shock certification requirement during 10/83. CIC's 42806-246 (add Juniper wire, add diode CR223, change resistor R501), 42806-345-3 (eliminate interferences with P1933, 42808-718 (overstrapped resistor R303 due to delta data logger, software change, diode VR201 rewiring), 42804-942 and 42806-942-1 (transistor D201 lead stress relief), have been incorporated and certified by similarity or analysis since this configuration was tested.

D. Inspection -

Each circuit board, the flex tape, and connectors are inspected for damage and contamination prior to being placed into finished stores. The CNS assembly is inspected internally and externally for damage and contamination during item assembly and externally during AIP. All soldering is inspected by HS OA and DCAB QA per MM3500.4 (3A-1).

E. Failure history -

EMU-150-0007 (8-2-83) WTRAM was not retained after a 10 second power interruption during in-process testing. The cause was found to be inadvertent resetting and reactivation of the microprocessor during the power off state due to capacitor discharging. Change 42804-246-1 was issued to change one resistor and add a diode to keep the microprocessor off.

F. Ground Turnaround -

Tested per EMU-R-001, DOT 616. Light verification.

G. Operational Use -

Crew Response - PreEVA: trouble shoot problem using RMS, if no success, consider EMU 3 if available. EMU no go for EVA.

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		2/2	150FMOD:		

EVA: When QWS issues BIVE indication and RIDS confirms invalid EMU DATE data, terminate EVA.
Training - Standard EMU training covers this failure mode.
Operational Considerations - Flight rules define operational EVS as at least able to monitor a valid status limit. EVA checklist procedures verify hardware integrity and systems operational status prior to EVA. Real Time Data System allows ground monitoring of EMU systems.